



PATENT APPLICATION

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Applicant:
Filing Date:

9898-292 Serial No. 10/632,572
Il-Man BAE, Jae-Hoon KIM, Jae-Hyeong LEE
August 1, 2003 Group: 2818

**INFORMATION DISCLOSURE CITATION
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U.S. PATENT DOCUMENTS

Exam Init	Ref	Document Number	Issue Date	Name	Class	Sub Class
11/11		5,812,491	9-22-1998	Shinozaki et al.		
		5,887,162	3-23-1999	Williams et al.		
		5,896,551	4-20-1999	Williams et al.		
		5,905,909	5-18-1999	Williams et al.		
		6,084,803	7-04-2000	Sredanovic et al.		
		6,178,501	1-23-2001	Ingalls		
11/11		US2001/0000820 A1	5-3-2001	Keeth et al.		

FOREIGN PATENT DOCUMENTS

<u>Exam</u> <u>Init</u>	<u>Document</u> <u>Number</u>	<u>Publication</u> <u>Date</u>	<u>Country</u>	<u>Name</u>
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OTHER DOCUMENTS

Exam **Init** **Ref** **Author, Title, Date, Pertinent Pages, Etc.)**

HNR Micron Technology, Inc., "MT41LC256K32D4 Synchronous Graphics Ram," Preliminary Specification booklet, 45 pgs., 1997.

Examiner: H. Ho

Date Considered: 3/12/05